



Review (Checklist)

Provides a customizable and structured method for checking the PCB Design database prior to sending out for board fabrication. This helps avoid a variety of costly issues later downstream when the board is built, tested and sold.

Features:

- Customizable for company specific requirements.
- Saves settings in the .brd file for history and record keeping.
- Detailed additional text and graphical information available for each check item.

dal review (Checklist) Beta #1 10-19-2018

Review Directory (default env -> dts_review_csv): L:/Cadence/dalTools/DTS_1.0/share/site/samples/review/review

Review Checklist File: (.csv) review_master_template.csv

Design Name: /Widget

Part Number: 123-1234-123

Revision: B

Board Designer: John Doe Signature: Date (mm-dd-yyyy): 10-18-2018

Design Eng: Jane Doe Signature: Date (mm-dd-yyyy): 10-19-2018

Mechanical Eng: Bill Cane Signature: Date (mm-dd-yyyy): 10-20-2018

Test Eng: Bob White Signature: Date (mm-dd-yyyy): 10-21-2018

Manufacture Eng: Jose Canas Signature: Date (mm-dd-yyyy): 10-22-2018

Pass	Fail	N/A	#	Description	Comment	Note Filename	Picture Filename
				--- Fab Drawing Checks ---			
<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	1	Drawing number and name correct on fab drawing.		dwgNum.txt	dwgNum.jpg
<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	2	Revision is correct.			
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	3	Stackup defined on fab drawing and correct.			
<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	4	Impedance control defined correctly.			
<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	5	Lines and spaces database constraints correct for intended impedance.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	6	Trace width altered slightly for impedance controlled signals on layers with mixed signals to allow easy search.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	7	Buried via layer pairs defined on stackup.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	8	Fabrication notes clear and correct.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	9	Board surface finish called out on fab drawing and appropriate for intended application.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	10	Material defined correctly.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	11	RoHS 6/6 requirements addressed in material and notes.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	12	Filleting allowed on traces/included if allowed.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	13	Note regarding trace width change present.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	14	Drill plan figures match drill legend.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	15	Netsort program run			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	16	Drill list quantities agree with drill legend.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	17	Hole tolerances correctly defined on fab drawing.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	18	Dimensions on fab drawing match board database.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	19	Board matches profile drawing or mechanical input.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	20	Tooling hole locations defined on fab drawing and correct.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	21	Artwork labels correct and listed correctly on fab drawing			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	22	.emn file has been re-imported and reviewed in ProE.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	23	Board thickness checked against connector spec requirements			
				--- Artwork Checks ---			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	24	Padstacks are correct.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	25	TDR lines on board and labeled.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	26	DRC clearance checks - including same net DRC's.			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	27	DRC connectivity checks			
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	28	DRC High speed rules checks			

Ok Cancel Page [BEG/DOWN/UP/END] << < > >> Editor ? Other -> Debug -> Export.xml Help

Video demonstration available at:

www.dalTools.com